	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	109 31	inverter same resonan$2 (ignit$3 near4 (capacitor capacitance))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 13:42	
2	BRS	L2	52	inverter same resonan$2 same (ignit$3 near4 (capacitor capacitance))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 14:14	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
3	BRS	L3	52	2 and 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 15:37	
4	BRS	L4	19	2 same parallel	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 16:51	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L5	234	dc adj3 link adj3 current	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 16:51	
6	BRS	L6	163	dc adj link adj current	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 16:52	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	L7	2	6 and 315/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/10 16:52	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L7	52	inverter same resonan$2 same (ignit$3 near4 capacitor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/08 16:42	
2	BRS	L8	7	7 same (short$2 open$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/08 17:23	

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(3) The ckt arrangement according to....
once the act arrangement has...

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
3	BRS	L9	19	7 and ((short$2 open$3) near3 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/08 17:24	
4	BRS	L10	16	9 and 315/$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/08 17:25	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
5	BRS	L11	14	9 not 8	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/04/08 17:25	